

10/1789, 406

JL
6/1/05OK to enter
J-LAmendments of the Specification

Please replace Paragraph [0022] with the following amended Paragraph [0022]:

[0022] In another embodiment, clock signal 130 may not be sent, or may only be a reference, and a separate clock may be derived directly from each data signal 140, 150, etc., by clock data recovery techniques such as those described in copending, commonly-assigned United States Patent Applications Nos. 09/805,843, filed March 13, 2001, and 10/059,014, filed January 29, 2002, and ~~10/093,785, filed March 6, 2002 in~~ commonly-assigned U.S. Patent 6,650,140, which are hereby incorporated by reference in their entireties, in which case the respective dynamic phase aligner 121, 122, etc. of each channel would be replaced by respective clock data recovery circuitry (not shown).